

# Home-School Learning Collaboration – Computing

Topics in this cycle: Boolean logic		Taught: Autumn 2	Year Group: 8
Key knowledge/concepts to be learnt ('Tell me about....')			Websites/blogs/YouTube links and further reading to deepen and consolidate learning
<ul style="list-style-type: none"><li><b>What is Boolean and how do we use it??</b> Identify what Boolean is and be able to explain what Boolean logic is. To understand the Boolean operators. Apply Boolean logic to solve simple problems</li><li><b>What are truth tables?</b> To be able to complete truth tables. Be able to use AND, OR, and NOT correctly.</li><li><b>What are logic gates?</b> Recognize and use simple logic gates Recognize logic gate symbols Apply logic gates to basic real-life problems</li><li><b>What are logic circuits?</b> Understanding that logic circuits are diagrams that use logic gates Work out the output of a simple logic circuit knowing the input</li></ul>			<b>Notes/Information</b>  Boolean & logic gates <a href="#">Boolean Logic &amp; Logic Gates: Crash Course Computer Science #3</a>  <b>Videos</b> Boolean lesson <a href="#">Boolean logic KS3</a>   <a href="#">Y8 Computing Lesson Resources</a>   <a href="#">Oak National Academy</a>
Key Vocabulary and Definitions To Be Learnt			What Will The Assessment Look Like?
Boolean	An expression that evaluates to either True or False		This unit will be assessed during assessment week. The questions on this topic will be as follows: <ul style="list-style-type: none"><li>Identifying logic gates.</li><li>Combining logic gates to create a logic circuit.</li><li>Completing a truth table for a logic gate or logic circuit.</li></ul>
Logic gates	An electronic component that carries out a logical operation		
Logic circuits	A circuit that is made from logic gates		
Truth tables	A table showing the outputs for all possible combinations of inputs to a logic gate or logic operation.		<b>Family Learning Opportunities</b>  Create a DIY puzzle game with family Make paper cards for 0 and 1, and pretend to be a logic gate:  Parent gives inputs A and B  Student acts like the AND or OR gate and gives the output